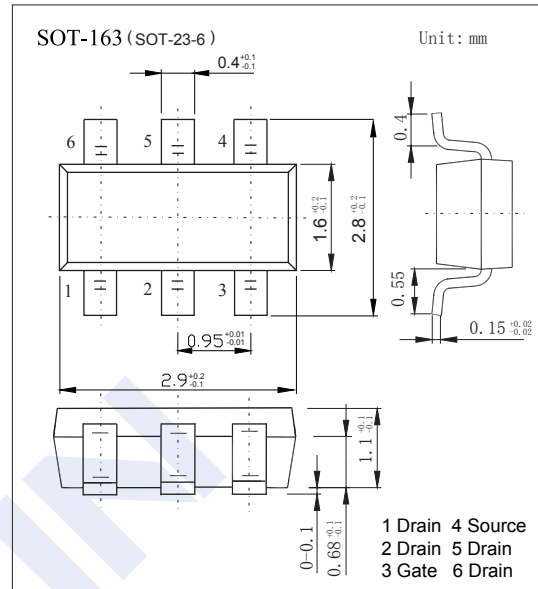
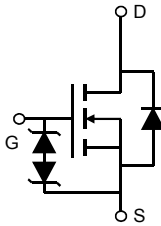


## N-Channel MOSFET

### AO6404 (KO6404)

#### ■ Features

- $V_{DS} = 20V$
- $I_D = 8.6 A$  ( $V_{GS} = 10V$ )
- $R_{DS(ON)} < 17m\Omega$  ( $V_{GS} = 10V$ )
- $R_{DS(ON)} < 18m\Omega$  ( $V_{GS} = 4.5V$ )
- $R_{DS(ON)} < 24m\Omega$  ( $V_{GS} = 2.5V$ )
- $R_{DS(ON)} < 33m\Omega$  ( $V_{GS} = 1.8V$ )
- ESD Rating: 2000V HBM



#### ■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	20	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Continuous Drain Current	$I_D$	$T_A=25^\circ C$	8.6
		$T_A=70^\circ C$	6.8
Pulsed Drain Current	$I_{DM}$	30	A
Power Dissipation	$P_D$	$T_A=25^\circ C$	2
		$T_A=70^\circ C$	1.28
Thermal Resistance.Junction- to-Ambient	$R_{thJA}$	$t \leq 10s$	62.5
		Steady-State	110
Thermal Resistance.Junction- to-Lead	$R_{thJL}$	50	$^\circ C/W$
Junction Temperature	$T_J$	150	$^\circ C$
Storage Temperature Range	$T_{stg}$	-55 to 150	$^\circ C$

## N-Channel MOSFET

## AO6404 (KO6404)

## ■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V <sub>DSS</sub>	I <sub>D</sub> =250 μA, V <sub>GS</sub> =0V	20			V
Gate-Source Breakdown Voltage	BV <sub>GSO</sub>	V <sub>DS</sub> =0V, I <sub>G</sub> =±250μA	±12			
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =16V, V <sub>GS</sub> =0V			10	μA
		V <sub>DS</sub> =16V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C			25	
Gate-Body Leakage Current	I <sub>GBSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±10V			±10	μA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250 μA	0.5		1	V
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =8.5A			17	mΩ
		V <sub>GS</sub> =10V, I <sub>D</sub> =8.5A T <sub>J</sub> =125°C			20	
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =5A			18	
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =4A			24	
		V <sub>GS</sub> =1.8V, I <sub>D</sub> =3A			33	
On State Drain Current	I <sub>D(on)</sub>	V <sub>GS</sub> =4.5V, V <sub>DS</sub> =5V	30			A
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =8A		36		S
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =10V, f=1MHz		1810		pF
Output Capacitance	C <sub>oss</sub>			232		
Reverse Transfer Capacitance	C <sub>rss</sub>			200		
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz		1.6		Ω
Total Gate Charge	Q <sub>g</sub>	V <sub>GS</sub> =4.5V, V <sub>DS</sub> =10V, I <sub>D</sub> =8.5A		17.9		nC
Gate Source Charge	Q <sub>gs</sub>			1.5		
Gate Drain Charge	Q <sub>gd</sub>			4.7		
Turn-On DelayTime	t <sub>d(on)</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =10V, R <sub>L</sub> =1.2Ω, R <sub>G</sub> =3Ω		2.5		ns
Turn-On Rise Time	t <sub>r</sub>			7.2		
Turn-Off DelayTime	t <sub>d(off)</sub>			49		
Turn-Off Fall Time	t <sub>f</sub>			10.8		
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 8.5A, di/dt= 100A/us		22		nC
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			9.8		
Maximum Body-Diode Continuous Current	I <sub>S</sub>				2.9	A
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1A, V <sub>GS</sub> =0V			1	V

\* The static characteristics in Figures 1 to 6 are obtained using <300us pulses, duty cycle 0.5% max.

## ■ Marking

Marking	D4**
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## N-Channel MOSFET AO6404 (KO6404)

■ Typical Characteristics

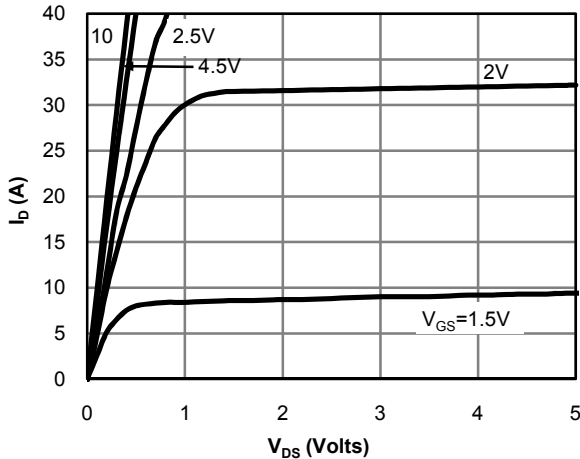


Fig 1: On-Region Characteristics

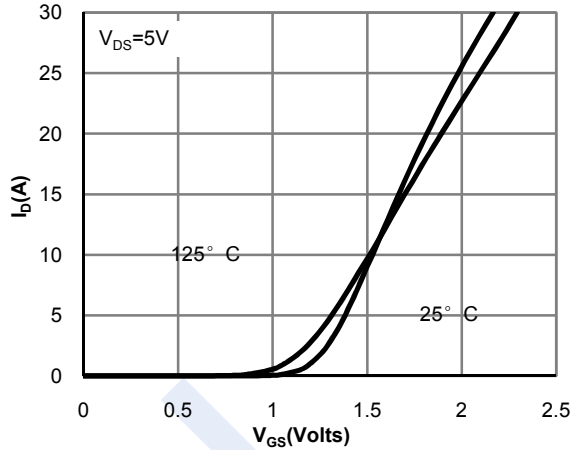


Figure 2: Transfer Characteristics

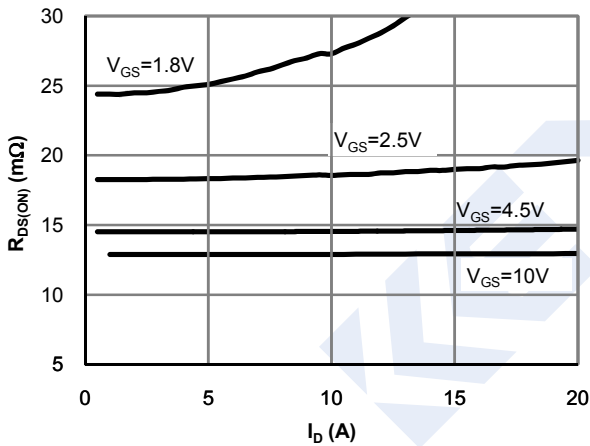


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

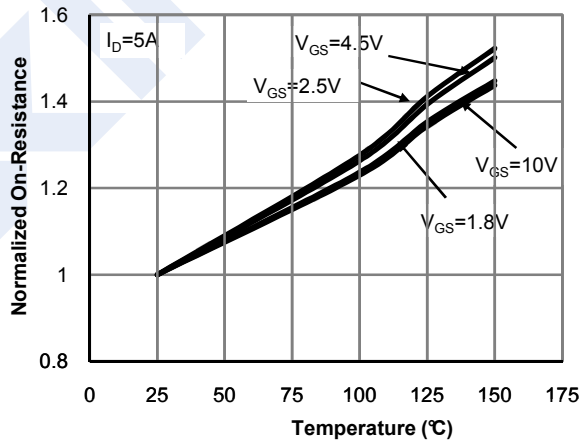


Figure 4: On-Resistance vs. Junction Temperature

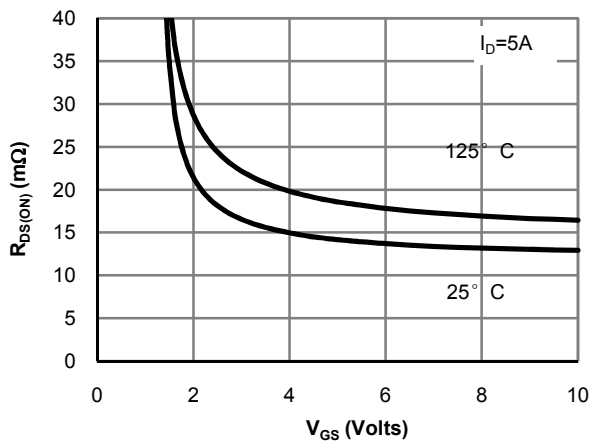


Figure 5: On-Resistance vs. Gate-Source Voltage

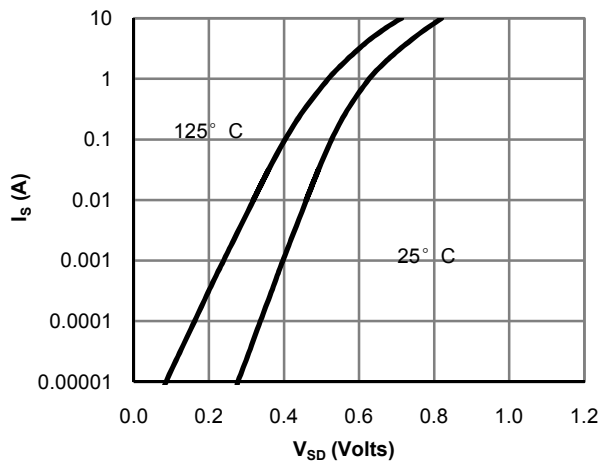


Figure 6: Body-Diode Characteristics

## N-Channel MOSFET AO6404 (KO6404)

■ Typical Characteristics

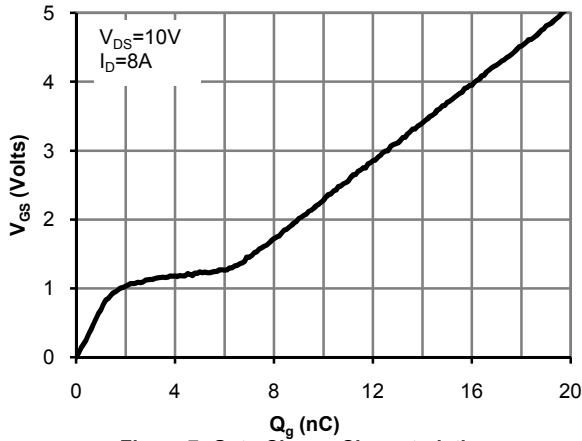


Figure 7: Gate-Charge Characteristics

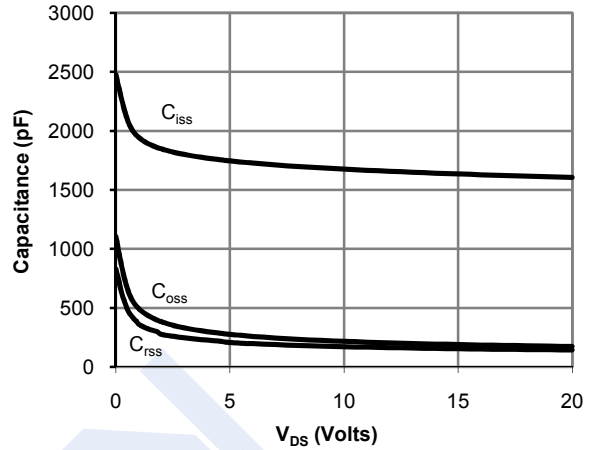


Figure 8: Capacitance Characteristics

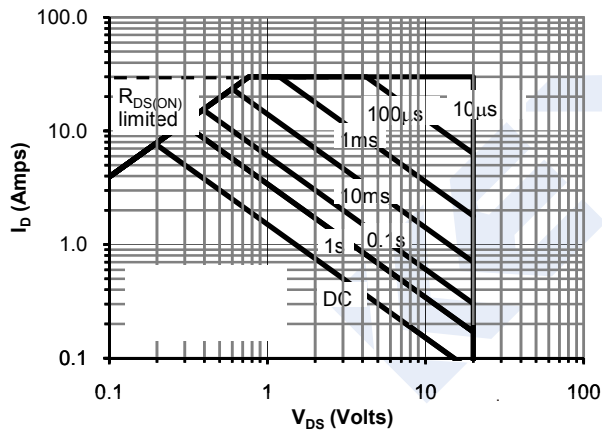


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

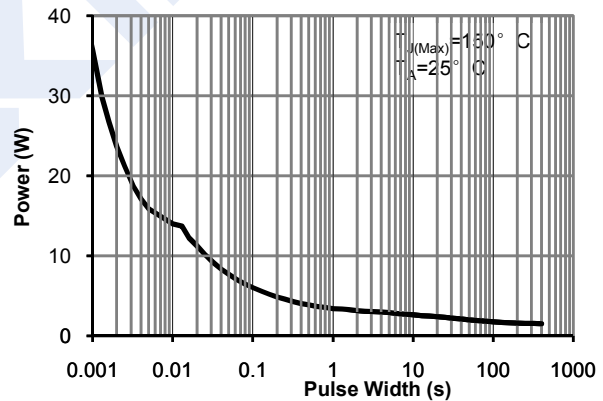


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

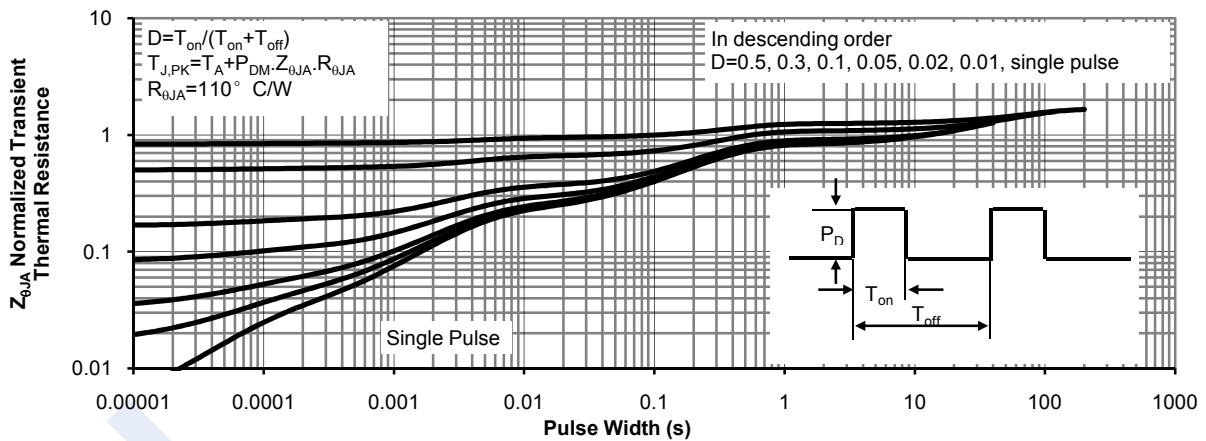


Figure 11: Normalized Maximum Transient Thermal Impedance